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L2 L1.clm. 2 L2

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<u>L2</u>	L1 and (without adj2 access\$3)	22	<u>L2</u>
<u>L1</u>	DMA near10 ASIC	324	<u>L1</u>

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<u>L4</u>	L3	0	<u>L4</u>
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<u>L3</u>	L1 and (without adj3 access\$3)	25	<u>L3</u>
<u>L2</u>	L1 and (without adj2 access\$3)	22	<u>L2</u>
<u>L1</u>	DMA near10 ASIC	324	<u>L1</u>

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L5 710/22,300,308,72,74,240,241,113;711/100,202;709/208,212.ccls.

6782 L5

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L4 L3

0 L4

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 L1 and (without adj3 access\$3)

25 L3

L2 L1 and (without adj2 access\$3)

22 L2

L1 DMA near10 ASIC

324 L1

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L3 and L5	12

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L6 L3 and L5

12 L6

L5 710/22,300,308,72,74,240,241,113;711/100,202;709/208,212.ccls.

6782 L5

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L4 L3

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DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 L1 and (without adj3 access\$3)

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L2 L1 and (without adj2 access\$3)

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324 L1

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9	<input type="checkbox"/>	<input type="checkbox"/>	US 6179489 B1	20010130	173	Devices, methods, systems and software or	718/102	
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- ☐ 1. **The Equator MAP-CA/spl trade/ DSP: an end-to-end broadband signal processor/spl trade/ VLIW**
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- ☐ 4. **QCDOC: A 10 Teraflops Computer for Tightly-Coupled Calculations**
 Boyle, P.A.; Chen, D.; Christ, N.H.; Clark, M.; Cohen, S.; Zhihua Dong; Gara, A.; Balint Joo; Chulwoo Jung; Levkova, L.; Xiaodong Liao; Guofeng Liu; Mawhinney, R.D.; Ohta, S.; Petrov, K.; Wettig, T.; Yamaguchi, A.; Cristian, C.;
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